

<b>Notice of References Cited</b>	Application/Control No. 10/621,908		Applicant(s)/Patent Under Reexamination DHONG ET AL.	
	Examiner Benjamin P. Geib		Art Unit 2181	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-6,282,556	08-2001	Chehrazi et al.	708/521
*	C	US-2004/0199751	10-2004	Johnson et al.	712/221
*	D	US-6,243,803	06-2001	Abdallah et al.	712/210
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"AMD 3DNow! Technology: Architecture and Implementations"; Stuart Oberman, Greg Favor, and Fred Weber; 1999, IEEE
	V	"IA-64 Application Developer's Architecture Guide"; Intel; 1999; pages 7-140, 7-141, 7-152, 7-153
	W	"MMX Technology Architecture Overview"; Millind Mittal, Alex Peleg, Uri Weiser; 1997; Intel Technology Journal
	X	"AltiVec: Bringing Vector Technology to the PowerPC Processor Family"; Jon Tyler, Jeff Lent, Anh Mather, Huy Nguyen; 1999; IEEE

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.